Welcome to Digital System Design. This course provides you with an introduction to the subject of combinational and sequential logic circuits and shows you how to design logic circuits using classical methods as well as hardware description language for modern computer aided design (CAD) tools. As such, even though it will provide only moderate depth on a wide variety of fundamental topics, it will retain full technical rigor throughout.

Topics: number systems; Boolean algebra; analysis, design, and minimization of combinational logic circuits; analysis and design of synchronous and asynchronous finite state machines; and introduction to VHDL and behavioral modeling of combinational and sequential circuits.

ECE332: This lab course should be taken concurrently with ECE331. It emphasizes the lecture material of ECE331 through the use of logic synthesis and simulation software, breadboarding and testing digital circuits and exploring the fundamental electrical characteristics of logic gates.

Faculty: Craig Lorie  Instructor  clorie@gmu.edu
Teaching Assistants: Rajesh Velegalati  Grader, Recitation, and Lab  rvelegal@gmu.edu
Shaunak Shah  Recitation and Lab  ssahahe@gmu.edu

Lectures: 4:30 – 5:45 pm, Tuesday and Thursday  FAB - B108

Recitations: Section 301  1:30 – 2:20 pm, Friday  Thompson - 121
Section 302  6:20 - 7:10 pm, Tuesday  ST2 - 260
Section 303  12:30 – 1:20 pm, Monday  Krug - 209


Office Hours:
Please check the class web page for the current office hour schedule. You should feel free to approach Dr. Lorie and the TAs at any time if you need help in addition to the scheduled sessions. The best way to contact us is via e-mail.

Recommended Background:
- ECE 280 Electric Circuit Analysis
- ECE 332 Digital Electronics and Logic Design Lab (can be taken concurrently)

Attendance
Attendance at recitation is required and will be randomly recorded throughout the semester by the recitation instructor. Attendance is not taken at lecture but class participation will be noted by the instructor.

You are responsible for all material presented in class and in the textbook.
Homework:
Homework will be assigned on a weekly basis (12 assignments total). Homework is due on Thursday and is to be handed in on paper at the beginning of class. Homework is very helpful in preparation for exams and is required to achieve an ‘A’ in this class. Homework must be handed in on time to receive credit. No late submission is possible.

Exams:
There will be two exams during the semester. Exams will be closed book. A single (two-sided) blank note card (3” x 5”) will be provided by the instructor on which you can write down your own notes. You are not allowed to use more than one card or to attach anything to this card. Your notes have to be hand written.

There will be NO make-up exams. (See Dr. Lorie for an exception.) Students who are more than 15 minutes late for an exam may not be admitted and will be assigned a grade of zero for the exam.
- Midterm Exam: Tuesday March 17th.
- Final Exam: Tuesday, May 12th from 4:30 – 7:15pm.

Grading:
The final grade is based on a weighted sum of your performance in exams, homeworks, recitations and class participation:

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Honor Code:
All rules of the GMU Honor Code system will be in effect. You must review the rules and be familiar with them.

You are encouraged to discuss homework problems with other students and/or obtain the assistance of the recitation instructor. Nevertheless, please write down your own solutions which represent your understanding of the material. Duplicating someone else’s homework solutions, hardware/software designs, diagrams, source code, and exam notes is considered cheating. If you use material from other sources such as but not limited to the web, books, journals, data sheets, etc. you must reference the source. Honor code violations will be followed up with full force.

Classroom Etiquette
Cellphones have to be turned off during class or at least put into silent mode. If you have an emergency need to answer a call please quietly leave the room BEFORE answering the call.

Lectures may not be recorded without express written permission from the instructor.

Students with Disabilities
If you need special assistance, please inform the instructor soon so that we can work something out.

The Course Syllabus is Subject to Change
# Spring 2009

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- Intro
- ActiveHDL
- Experiment 1
- 2.9 – 2.10
- 2.5 – 2.8
- Experiment 2
- 4.1 – 4.5
- 4.6 – 4.7
- Experiment 4
- 5.1; 5.2 – 5.3
- Experiment 8a
- 5.2 – 5.3
- 5.4 – 5.5
- Experiment 8b
- 5.6 – 5.7
- 6.1 – 6.2
- Experiment 6
- 6.3 – 6.7
- Exam Prep
- Midterm 332
- Exam Prep
- Experiment 5
- 9.6;
- 3.2 – 3.3
### Experiment 7
Lecture 18
Power

### HW7 Due
3.2 – 3.3; 3.8

### Experiment 8
Lecture 19
Delay

### HW8 Due
7.1 – 7.4

### Experiment 9
Lecture 20
D-FF

### HW9 Due
7.5 – 7.6; 7.8

### Experiment 10
Lecture 21
Register

### HW10 Due
7.8 – 8.1; 8.7

### Final Exam
Lecture 22
State Machine

### HW11 Due
9.1 – 9.5

### Final Exam
Lecture 23
Async State Machine

### HW12 Due
8.1 – 8.7; 9.1 – 9.5

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**The Course Schedule is Subject to Change!!!**