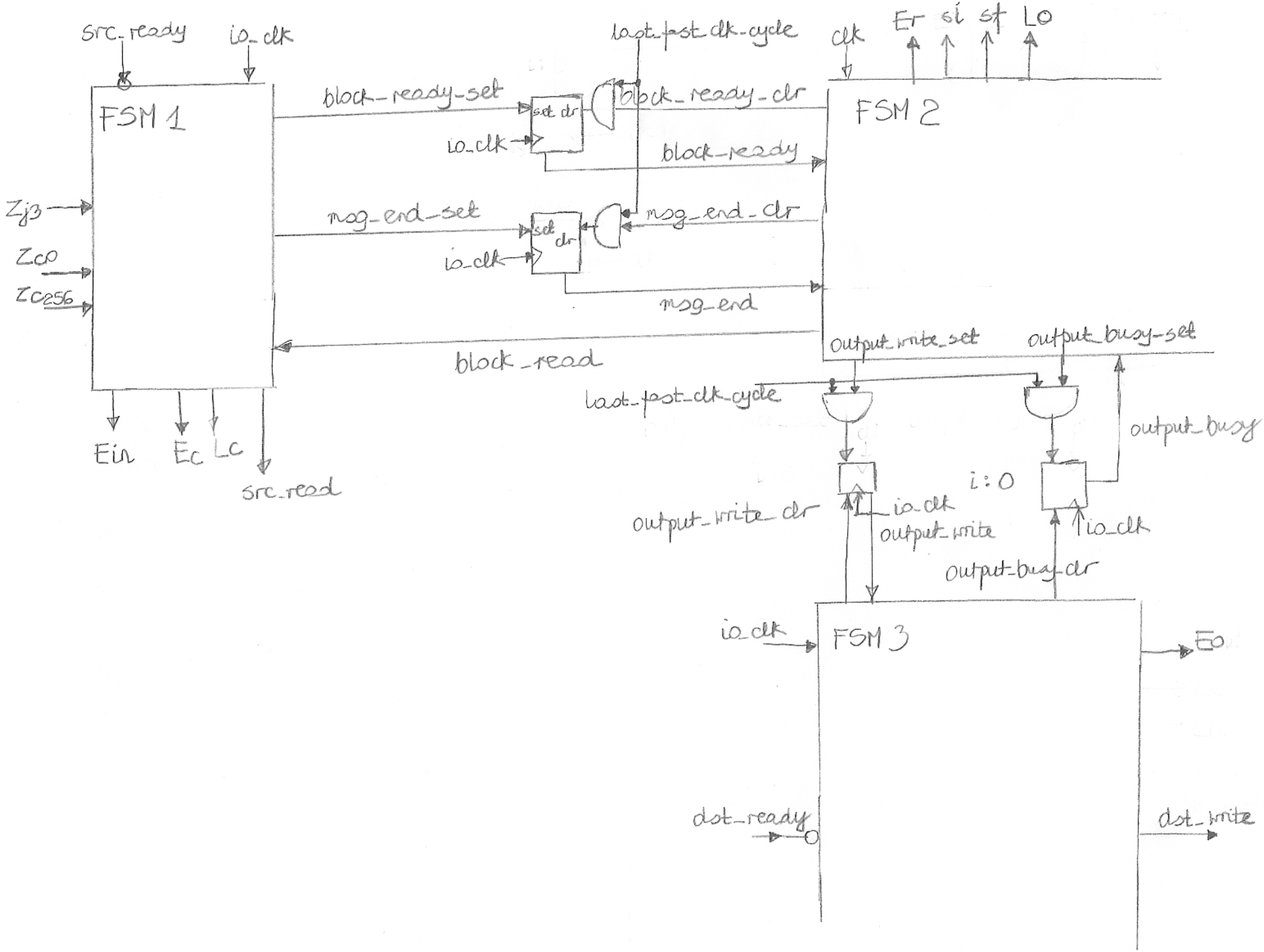
src_ready
io_clk
FSM 1
Zj3
Zc0
Zc256
Ein
Ec
Lc
src_read
block_ready_set
set dr
io_clk
last_fast_clk_cycle
block_ready_clr
block_ready
msg_end_set
set dr
io_clk
msg_end_clr
msg_end
block_read
clk
Er
si
st
Lo
FSM 2
output_write_set
output_busy_set
last_fast_clk_cycle
output_busy
i:0
output_write_clr
io_clk
output_write
io_clk
output_busy_clr
io_clk
FSM 3
Eo
dst_ready
dst_write