

Fall 2008

Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	Sunday	Labs/Reading
August 25 Lecture 1 Introduction	26	27 Lecture 2 Jump Start	28	29	30	31	Intro 1, 2.1-2.4, 2.9-2.10
September 1 Labor Day	2	3 Lecture 3 Boolean Alg. HW1 Due	4	5	6	7	ActiveHDL Experiment 1 2.5-2.8 4.1-4.5
8 Lecture 4 K-Maps	9	10 Lecture 5 K-Map/Design HW2 Due	11	12	13	14	Experiment 2 and 3 5.1
15 Lecture 6 Number Systems	16	17 Lecture 7 Comp Arith HW3 Due	18	19	20	21	Experiment 4 5.2-5.3, 5.8
22 Lecture 8 Codes and Design	23	24 Lecture 9 Adder in VHDL HW4 Due	25	26	27	28	Experiment 8 5.4-5.7
29 Lecture 10 Multi-bit Adder	30	October 1 Lecture 11 Mux/Decoder HW5 Due	2	3	4	5	Experiment 6a 6.1-6.7
6 Lecture 12 Encoder	7	8 Lecture 13 Review HW6 Due	9	10	11	12	Experiment 6b Exam Prep
13 Columbus Day Recess	14 Monday Class Midterm	15 Lecture 14 Testbench	16	17	18	19	Midterm 332
20 Lecture 15 Hazard	21	22 Lecture 16 CMOS HW7 Due	23	24	25	26	Experiment 5
27 Lecture 17 TTL Interfacing	28	29 Lecture 18 Power HW8 Due	30	31	November 1	2	Experiment 7
3 Lecture 19 Delay	4	5 Lecture 20 D-FF HW9 Due	6	7	8	9	Experiment Shift Register
10 Lecture 21 Other Flip-Flops	11	12 Lecture 22 Register HW10 Due	13	14	15	16	Experiment 9

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November 17 Lecture 23 Sequencer	18	19 Lecture 24 State Machine HW11 Due	20	21	22	23	Experiment 10 and Review
24 Lecture 25 State Machine	25	26	27	28	29	30	
Thanksgiving Recess							
December 1 Lecture 26 Asynchronous State Machine	2	3 Lecture 27 Review HW12 Due	4	5	6	7	Final Exam 332
8	9	10 Final Exam	11	12	13	14	
15	16	17	18	19	20	21	

The Course Schedule is Subject to Change!!!