

# ECE 746 Project Presentations

Tuesday, May 10th, 2011

7:20 - 10:40 PM

Engineering Building, Room 3507

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## Session I

### Low Area Implementations of SHA-3 Finalists

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7:20	-	7:30	Welcome & Rules of the Contest for the Best Project <i>Jens-Peter Kaps</i>	
7:30	-	7:50	Implementing low area SHA-3 candidates on Xilinx FPGA platforms using Partial Reconfiguration <i>Ahmad Salman</i>	[Specifications] [Presentation]
7:50	-	8:10	Low Area Implementation of Skein <i>Bilal Habib</i>	[Specifications] [Presentation]
8:10	-	8:30	Low Area Implementation of Keccak with Padding <i>Smriti Gurung</i>	[Specifications] [Presentation]

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## Session II

### SHA-3 Finalists Implementation Considerations

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8:40	-	9:00	Use of Embedded FPGA Resources in Implementations of SHA-3 Candidates <i>Rabia Shahid and Malik Umar Sharif</i>	[Specifications] [Presentation]
9:00	-	9:20	Power measurements of SHA-3 Finalists <i>Kishore Kumar Surpathi</i>	[Specifications] [Presentation]
9:20	-	9:40	CUIDE: Control Unit Integrated Development Environment <i>Ekawat Homsirikamol</i>	[Specifications] [Presentation]

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## Session III

### Benchmarks

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9:50	-	10:10	FIPS OpenSSL Benchmark on Modern Smartphones <i>Zhaohui Wang</i>	[Specifications] [Presentation]
10:10	-	10:30	Porting and optimization of SHA3 finalists to XBX-MSP430 platform <i>John Pham</i>	[Specifications] [Presentation]
10:30	-	10:40	Announcements & Closing Remarks	

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