ECE 448
Final Exam
Problems

1. (pseudocode, interface) => block diagram
2. (pseudocode, interface, block diagram) => ASM chart
3. actions => control signals
4. block diagram of a system involving PicoBlaze based on the provided specification
5. analysis of the PicoBlaze code
6. timing waveforms based on ASM chart & inputs
7. utilization of FPGA resources
Final Exam

Tuesday, May 15, 4:30-7:15 PM
Review Session

Monday, May 14, 11:00AM-2:00PM