Final Exam
Final Exam – 25 points
Friday, May 8, 1:30pm-4:15pm
Part II – Typical Problems

1. (pseudocode, interface) => block diagram
2. (pseudocode, interface, block diagram) => ASM chart
3. actions => control signals (including “-”)
4. block diagram of a system involving PicoBlaze based on a provided specification
5. analysis of the PicoBlaze code
Part I – Typical Problems

1. timing waveforms based on an ASM chart & inputs
2. utilization of FPGA resources
3. VGA display
4. circuits with a regular structure (for-generate)
5. Xilinx FPGA memories
6. PicoBlaze organization and instruction set
7. fill in the blanks
8. VHDL code => block diagram