For the PicoBlaze-6 system, developed as part of the previous PicoBlaze IO Class Exercise 1, including:
- four input registers with the virtual addresses 00, 40, 80, and C0,
- four output registers with the virtual addresses C0, C4, C8, CC,
- the input register with the address C0 identical to the output register with the address C0

determine the contents of:
1. internal registers s0-s4
2. flags C, Z, I, preserved C, preserved Z
3. all input registers
4. all output registers
5. stack
6. PC

at the time of the execution of the instruction RETI ENABLE

before this instruction takes effect assuming that:
- at the time = 0 a short pulse is generated at the input RESET
- at the time = 10 seconds, a short pulse is generated at the input INT
- the contents of the instruction memory is given by the following program:

```assembly
BIT2 EQU 0x04
BIT7C EQU 0x7F
BIT5 EQU 0x20

ORG 0x000
LOAD s0, 0xFF
LOAD s1, 0xCC
LOAD s2, 0x04

INIT:
OUT s0, (s1)
SUB s0, 0x10
SUB s1, 0x04
SUB s2, 0x01
JUMP NZ, INIT
EINT

LOOP:
JUMP LOOP

ISR:
IN s3, 0xC0
LOAD s4, 0xAB
OR s4, BIT2
AND s4, BIT7C
XOR s4, BIT5
SRA s4
TEST s3, s4
RETI ENABLE

ORG 0x3FF
JUMP ISR
```