ECE 433: Linear Electronics - II  
Spring 2014

Instructor: Rao Mulpuri  
Nguyen Engineering Building, Room # 3237  
E-mail: rmulpuri@gmu.edu  
Office Hours: Tuesday and Thursday 4.30 pm - 6.00 pm

This course deals with design of multistage amplifiers, fundamental building blocks of analog integrated circuits and A/D and D/A converters. Two SPICE simulation based amplifier design projects will be given.

The following topics will be covered during the classes on indicated dates:

1. High frequency response of transistor amplifiers (1/21, 1/23, 1/28)
2. Differential and multistage amplifiers and their design (2/4, 2/6, 2/11, 2/13, 2/18, 2/20)
5. Frequency Response of Feedback amplifiers (3/27, 4/1)
7. A/D and D/A converters ( 5/1, 5/6)
9. SPICE aided design of transistor circuits

Grading:

Mid-semester exam - 35 %, Mid-semester Exam on 3/18  
Final exam - 40 %  
Projects - 15 %  
Home Work - 10 %

Late home works and projects will not be accepted. Make-up exams will not be given without prior approval. Only severe health problems or a valid family emergency will be considered to allow for a make-up exam.

Textbook: “Microelectronic Circuits” by Sedra and Smith, Oxford University Press, Sixth Edition